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Semiconductor Device Physics and Design UMESH K. MISHRA University of California, Santa Barbara, CA, USA and JASPRIT SINGH The University of Michigan, Ann Arbor, MI, USA

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2 MOSFET DEVICE PHYSICS AND OPERATION Gate Source Drain Semiconductor substrate Insulator Gate junction Substrate contact Conducting channel Figure 1.1 Schematic illustration of a generic field

effect transistor. This device can be viewed as a combination of two orthogonal two-terminal devices

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